

A Robust Control Approach for Frequency Support Capability of Grid-Tie Photovoltaic Systems

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ABSTRACT

Distributed solar Photovoltaic (PV) generation is growing rapidly around the world. However, unlike conventional synchronous generators, PV systems do not have any rotating masses to deliver inertia to support the grid frequency. The paper presents a detailed modeling of a new converter configuration and control scheme to enable PV systems to adjust the real power output and contribute to the grid frequency regulation. The proposed topology consists of a two-stage converter without an energy storage system. A dc-dc buck converter is used instead of a dc-dc boost converter, and this simplifies the control scheme which aims to keep the PV generator power in the right side of the P-V characteristic and can be varied in the range from near-zero to the maximum power. The proposed control scheme combines robust and nonlinear sliding mode theory with fuzzy logic. The PV system is connected to a low inertia microgrid and its ability to contribute to frequency regulation is assessed for different controls. The proposed converter and its control

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33 *are validated experimentally on a 3-kW PV system using OPAL-RT real-time simulator and tested under*
34 *varying temperature, solar irradiance, and partial shading conditions. The results show that with the*
35 *proposed circuit, the operating point is always on the right side of the P-V characteristic irrespective of the*
36 *operating mode. Furthermore, the proposed control scheme provides PV generators with a fast and effective*
37 *inertial response to support the grid and enhance its stability during contingencies.*

38 *Keywords: grid frequency regulation, inertial response, photovoltaic systems, partial shading, real power*
39 *control, fuzzy logic, sliding mode control.*

40 **1. INTRODUCTION**

41

42 The energy sector across the world is undergoing a profound transition from
43 carbon-intensive energy to low-carbon, renewable energy. This transformation is due to
44 the excessive exploitation of fossil fuels over the past decades which has generated
45 collateral effects causing global warming of the planet and a rapid decline in the
46 reserves of these natural resources. Among these renewable sources, solar energy is
47 one of the most abundant and will remain the ultimate energy source across all parts of
48 the world.

49 In recent years, photovoltaic (PV) technology has been the most prominent
50 technique for electricity generation and has an enormous potential to address the
51 global future energy challenges [1]. According to the International Renewable Energy
52 Agency (IRENA), grid-tied PV capacity reached 843.086 GW and off-grid reached 4.865
53 GW in 2021 [2]. The significant increase in PV installed capacity is mainly due to
54 technological improvements and government policies to introduce feed-in tariffs,
55 rebates, and incentives to promote the adoption of the PV systems in homes and
56 businesses which have considerably reduced their costs [3].

57 Therefore, the gradual replacement of conventional generators with PV
58 distributed generation will reduce the system's inertia and increase the sensitivity of the
59 grid frequency during power imbalance [4]. To overcome such a problem and ensure the
60 security of the electricity grid, many countries have revised their technical regulations
61 for connecting distributed generation sources to the electricity grid [5]. As a result, PV
62 systems are now required to participate in frequency regulation to enhance grid security
63 [6].

64 In this context, two approaches have been adopted. The indirect approach uses
65 a storage system; the PV generators (PVG) indirectly participate in the frequency
66 regulation. However, the indirect approach generally involves a high investment cost to
67 deploy the grid-scale energy storage facility [7]. The direct approach, on the other hand,
68 consists in modifying the control of the PVG to reduce their output power below the
69 maximum power. Thus, an amount of real power is reserved, and can be injected into
70 the grid in the event of frequency drop. Alternatively, if there is an increase in
71 frequency, the real power must be reduced. The direct method has been investigated by
72 several researchers, and the feasibility of this technique has been demonstrated on a
73 300 MW PV power plant in California [8]. The successful implementation of such a
74 technique requires (i) accurate real-time estimation of the available power of the PVGs,
75 (ii) rapid and effective response of the controlled real power [8].

76 Concerning the area related to rapid and effective real power response of PVGs,
77 it is a relatively new area of research, and very little work has been done. Rapid
78 response of the real power is useful in stabilizing the frequency, especially in a low

79 inertia grid [9]. The real power response must be less than one second (for example 0.5
80 sec [10] or 0.1 sec [11]), depending on the grid code. But it should be noted that with
81 classical control methods, the PVG power response is slow (greater than 1 sec) [8].
82 Recent research has focused on improving the real power response of PVGs. As an
83 example, in [12] the authors have investigated a PV-STATCOM smart inverter and have
84 shown the need for a rapid response of the real power to achieve a fast frequency
85 response. The control proposed by these authors is based on PI controllers tuned with a
86 systematic trial and error approach. In [9], the authors proposed a predictive control of
87 the dc-ac converter to ensure a very fast and accurate control of the real power. But
88 they pointed out that the proposed method required irradiance and temperature
89 measurements, which would increase the cost of the system. In addition, this method
90 produced transient and steady-state errors and therefore is not sufficiently accurate.

91 The authors in [9] and [12] used the same converter topology (single-stage
92 inverters), but the range of the DC output voltage of this converter type is limited [13].
93 So the range of PVG power variability will be limited, and its capacity to participate in
94 the grid frequency regulation will also be limited. To achieve a wider range (from near
95 zero to maximum power), the authors in [14] used a two-stage configuration based on a
96 dc-dc converter and a dc-ac converter. This configuration provides flexible operation
97 and the control of PV systems, because the DC link voltage of the PV inverter is
98 decoupled from the PVG voltage [15]. The two-stage configuration is widely used in
99 large-scale grid-tie PV systems [16]. Thus the dc-dc converter control needs to be
100 changed to control the PVG power. However, from the control perspective, it will make

101 the PVG participation in the grid frequency regulation more difficult [17]. Because the P-
102 V characteristic of a PVG is non-monotonic, therefore, it is recommended that the
103 operating point is varied in the right side of the PVG characteristic in order to have a
104 good performance of the PVG (this will be discussed in Section 4).

105 Several authors have tried to address the problem of keeping the operating
106 point in the right side of the PVG characteristic. In [18], the authors proposed to vary
107 the PVG power by a dc-dc boost converter and a PI controller to regulate the PVG
108 output voltage. However, they could not achieve a precise power tracking. So, in [17]
109 the authors proposed to add another PI controller to control both power and voltage.
110 But the suggested solution requires the use of three controllers, for the frequency,
111 power and voltage which need to be tuned properly to achieve the desired stability. In
112 addition, the authors did not show how to avoid the left side of the PV characteristic.

113 On the other hand, in [19] the authors proposed to use only a PI controller to
114 control the PVG power instead of controlling its voltage. But a single controller is
115 insufficient to keep the operating point in the right side of the PV characteristic. So, in
116 [14] the authors proposed to add an algorithm to the controller. The principle of this
117 algorithm is that the PI controller considers a modified version of the PV characteristic,
118 instead of the actual characteristic. The modified characteristic reflects the left side of the
119 PV characteristic as a horizontal line passing through the MPP (maximum power point).
120 So, if the operating point is located on the left side of the characteristic, then a negative
121 error results, which shifts the operating point to the right side of the characteristic.
122 However, this technique is faulty during partial shading. So, in [20], the authors propose

123 to improve the algorithm, but this has increased the complexity of the control. Because
 124 under partial shading conditions, the P-V characteristic becomes more complex which
 125 makes proper modification quite difficult.

126 To address the limitations of the above methods, our paper proposes to replace
 127 the dc-dc boost converter with a dc-dc buck converter. The buck converter requires a
 128 simple control of the PVG to operate only in the right side of its characteristic in the
 129 range from near-zero to the maximum power. This will increase the PVG capacity to
 130 participate in the grid frequency regulation. Furthermore, a robust Fuzzy Sliding Mode
 131 (FSM) controller is designed to control the PV power.

132 The remaining of the paper is organized as follows: Section 2 describes the PV
 133 energy conversion system connected to the power grid. Section 3 represents the
 134 technical issues of the grid code on the management of real power relating to frequency
 135 regulation. Section 4 and 5 present in detail the proposed buck converter topology and
 136 its control strategy respectively. Finally, the simulation results using OPAL-RT real-time
 137 simulator and the conclusion are presented in Sections 6 and 7 respectively.

138

139 **2. Description of the PV System Connected to the Electricity Grid**

140

141 The PV energy conversion system studied is based on two converters: dc/dc and
 142 dc/ac as shown in Fig. 1. This configuration is widely used in large-scale grid-tie PV
 143 systems [16].

144 The single diode model of the PVG is described by the following equation [21]:

$$145 \quad I_{pv} = N_p I_{sc} - N_p I_{sat} \left[\exp \left(\frac{1}{nV_t} \left(\frac{V_{pv}}{N_s} + \frac{I_{pv} R_s}{N_p} \right) \right) - 1 \right] - \frac{N_p}{R_p} \left(\frac{V_{pv}}{N_s} + \frac{I_{pv} R_s}{N_p} \right) \quad (1)$$

146

Where $V_t = KT/e$

147

The buck converter consists of a MOSFET which is connected to an anti-parallel

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diode. The inverter consists of three switching cells, each made up of two IGBT

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transistors connected to two anti-parallel diodes to provide bidirectional current flow. In

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this study, all switches are assumed ideal and therefore switching losses are neglected.

151

Filters are employed to eliminate the harmonics generated by the converters. Since a

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dc/dc buck converter is used, a transformer is required for the connection to the grid.

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The transformer allows, on the one hand, to raise the voltage supplied by the inverter to

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the desired value required by the grid, and on the other hand to provide galvanic

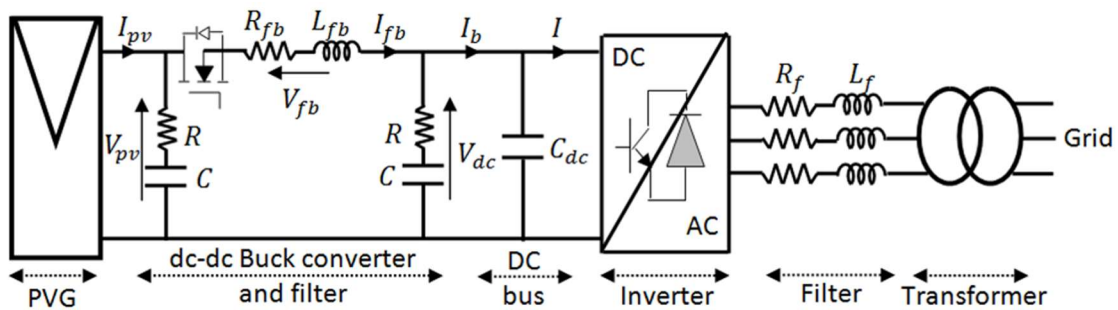
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isolation between the two energy sources. In this study, a three-phase Delta-Star

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connected 180 V /380 V transformer is used.

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Figure 1. Grid connected PV energy conversion system

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3. Grid Code Technical Issues on Real Power Management

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Currently, distributed PV generation is increasingly being called upon to comply

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with the requirements imposed by the grid operator. Among these requirements are the

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real power control modes relating to frequency regulation. In the Danish grid code [6],

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for example, there are different types of real power controls to connect PV plants to the

166 grid. These modes can be requested by the grid manager based on the system status
167 and operating conditions. There are five technical issues which are briefly summarized
168 in the Appendix 1.

169 **4. Proposed Solution to Operate PVGs at Variable Powers** 170 171

172 This section presents the solution proposed to enable PVGs to operate with
173 variable power and comply with the technical requirements imposed by the grid codes.

174 In Figure. 2 are shown typical I-V and P-V characteristics of a PVG for a given
175 temperature and irradiation. To control the PVG power over the entire range from near-
176 zero to the maximum power (P_{ref} dotted lines), the PVG output voltage V_{pv} must be
177 varied. Hence there are several possible operating points located on each side of the
178 MPP: the left operating points (circle marker) and the right operating points (lozenge
179 marker). However, two of these operating points (open circuit voltage V_{oc} and short
180 circuit current I_{sc}) must be avoided in order not to damage the PVG [22]. Clearly, the
181 operation of the PVG in the right side of the MPP (high voltage) is relatively better than
182 in the left side (low voltage). This is due to several reasons: to avoid the high current
183 which heats the PVG reducing its efficiency, and to have a faster dynamic response,
184 since on the right side, the slope of the characteristic P-V is steeper than on the left side
185 [14].

186 If the voltage of GPV is controlled by a dc-dc boost, and there are rapid changes
187 in irradiance and temperature, the operating point may be shifted to the left side
188 despite the working point is on the right side [14]. Thus, the proposed solution consists

189 of using a buck converter because it does not require an additional complex algorithm
 190 to the PVG control and it ensures that the PV system always operates on the right side
 191 of the characteristic under any operating conditions.

192 In fact, static converters connected to the grid generally operate at a constant
 193 DC bus voltage (V_{dc}) in order to have a better power flow. The duty cycle of the dc-dc
 194 converter is adjusted to vary the input voltage of the converter (V_{pv}). Since a buck
 195 converter is employed, then the input voltage value (V_{pv}) is always greater than or equal
 196 to the output voltage value (V_{dc}). Furthermore, to ensure that the PVG always operates
 197 in the right side of the MPP, the V_{dc} value must be kept equal or very close to the
 198 optimal value V_{opt} of the PVG.

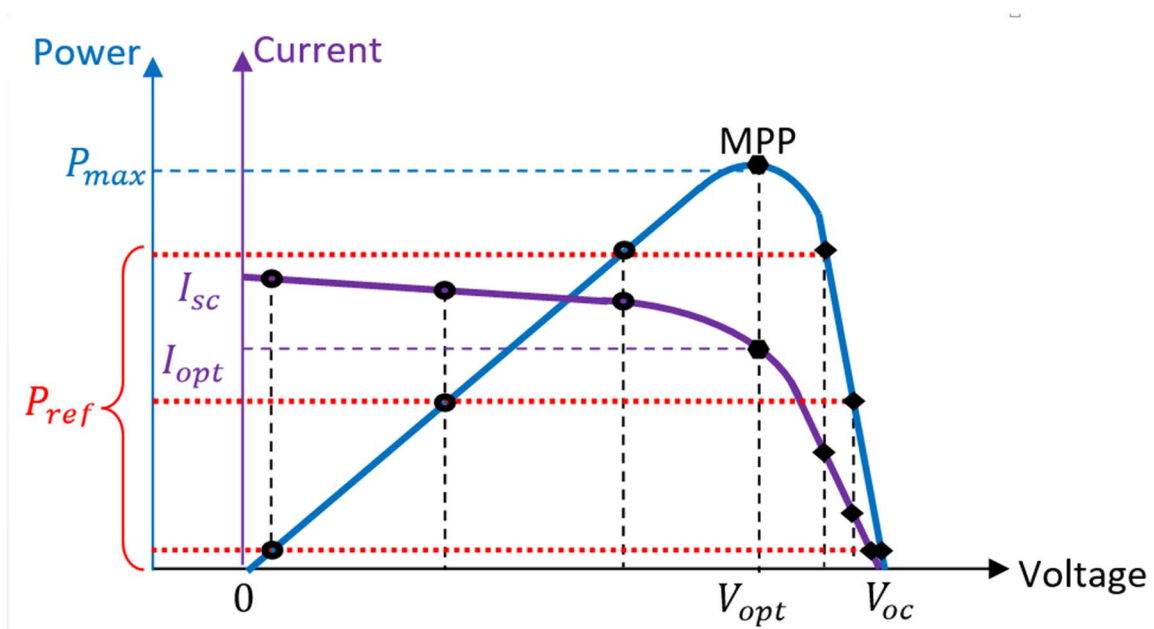


Figure 2. PVG I-V and P-V characteristics

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5. Control Scheme of the Grid-Tie PV Power System

204 With reference to Fig.1, two control schemes are therefore necessary to ensure
 205 the operation of the system: (i) control of the buck converter to regulate the power

206 produced by the PVG, (ii) control of the inverter via the regulation of the DC bus voltage
 207 and the real and reactive powers exchanged with the grid (the inverter control is
 208 detailed in the Appendix 2).

209
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 211

5.1. Overview of PV power control using classical PI regulator

212 To control the power produced by the PVG, it is necessary to control the output
 213 voltage V_{pv} of the PVG according to the desired reference power. This is achieved by
 214 adjusting the duty cycle α of the buck converter and by controlling the filter current I_{fb} .

215 To derive the control law, we use the equations of the buck converter and its
 216 filter [23].

217 When the switch is closed ($0 < t < \alpha T_s$):

$$218 \quad V_{pv} = R_{fb}I_{fb} + L_{fb} \frac{dI_{fb}}{dt} + V_{dc} \quad (2)$$

$$219 \quad \frac{dI_{fb}}{dt} = \frac{1}{L_{fb}} (V_{pv} - V_{dc} - R_{fb}I_{fb}) \quad (3)$$

220 When the switch is open ($\alpha T_s < t < T_s$):

$$221 \quad 0 = R_{fb}I_{fb} + L_{fb} \frac{dI_{fb}}{dt} + V_{dc} \quad (4)$$

$$222 \quad \frac{dI_{fb}}{dt} = -\frac{1}{L_{fb}} (V_{dc} + R_{fb}I_{fb}) \quad (5)$$

223 Equations (3) and (5) describe the dynamics of the current in the filter ($R_{fb}L_{fb}$)
 224 of the buck converter for the periods αT_s and $(1 - \alpha)T_s$ respectively. To find an
 225 approximate dynamic representation that is valid for both time intervals, it will be
 226 assumed that the current I_{fb} has a linear form, hence its derivative will be constant.

227 Based on this assumption, the expression of the mean value of the current can be
 228 decomposed over the two time periods αT_s and $(1 - \alpha)T_s$:

$$229 \quad \frac{dI_{fb}}{dt} \cdot T_s = \frac{dI_{fb}}{dt_{(\alpha T_s)}} \cdot \alpha T_s + \frac{dI_{fb}}{dt_{((1-\alpha)T_s)}} \cdot (1 - \alpha)T_s \quad (6)$$

230 Substituting Eq. (3) and (5) in (6) yields the equation which governs the system
 231 over an entire period:

$$232 \quad \frac{dI_{fb}}{dt} = \frac{1}{L_{fb}} (\alpha V_{pv} - V_{dc} - R_{fb} I_{fb}) \quad (7)$$

233 Thus, the control law can be deduced as:

$$234 \quad \alpha = \frac{V_{fb} + V_{dc}}{V_{pv}} \quad (8)$$

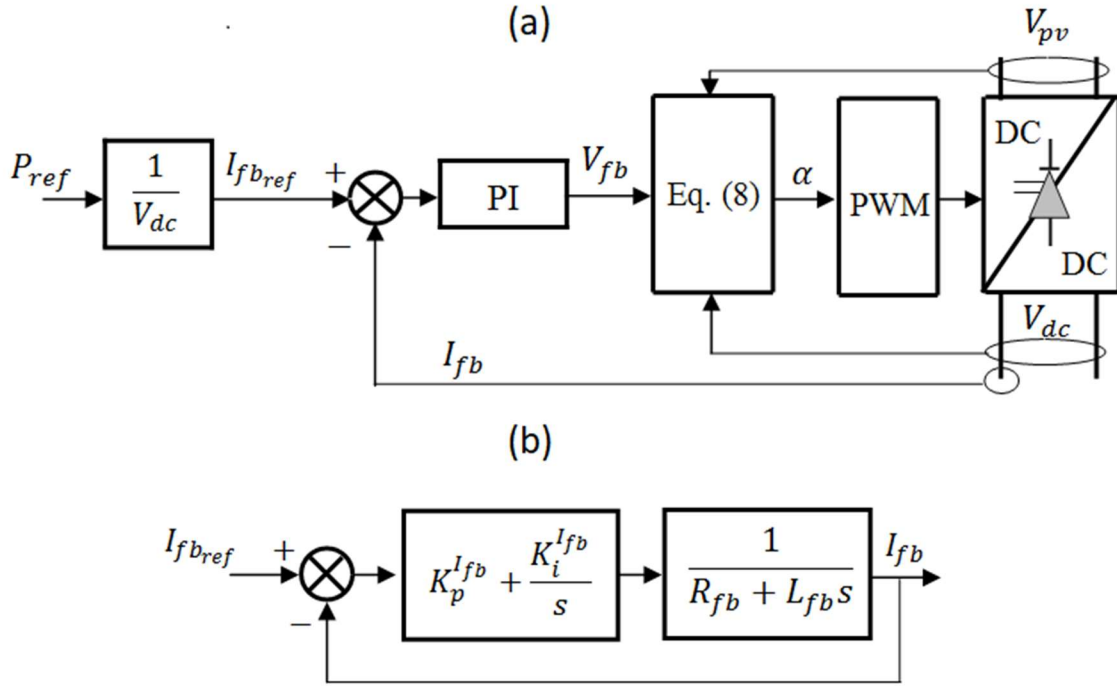
235 The purpose of the control is to find the duty cycle α for each reference power
 236 value. Since in our system the voltage V_{dc} is set by the dc/ac converter, therefore by
 237 varying the duty cycle, the voltage V_{pv} as well as the power of the PVG can be varied.
 238 Based on Eq. (8), the duty cycle can be varied by changing the filter voltage V_{fb} which is,
 239 in turn, dependent on the current flowing through the filter (I_{fb}). So, from the reference
 240 power, the reference current I_{fbref} can be calculated and to ensure continuous flow of
 241 the current during the power variation, a controller is required.

242 Figure 3.a shows the block diagram of the buck converter control.

243 The block diagram of the closed-loop control of the current I_{fb} is shown in
 244 Fig.3.b.

245 The PI controller parameters are calculated so that the closed-loop transfer
 246 function is first order with time-constant $t_s^{I_{fb}}$.

$$247 \quad \begin{cases} K_p^{I_{fb}} = \frac{3L_{fb}}{t_s^{I_{fb}}} \\ K_i^{I_{fb}} = \frac{3R_{fb}}{t_s^{I_{fb}}} \end{cases} \quad (9)$$



248

249 Figure 3. Classical control scheme of the buck converter: (a) general scheme, (b)

250

 I_{fb} current control block diagram

251

252 **5.2. Robust fuzzy sliding mode control**

253

254 Our paper proposes a hybrid control strategy combining the robust features of

255 sliding mode and fuzzy logic controllers to overcome the limitations of classical PI

256 controllers [24].

257 The sliding surface is defined as follows [25]:

$$258 \quad S(X) = \left(\frac{d}{dt} + \lambda \right)^{n-1} e \quad (10)$$

259 Where n is the relative degree to the drift number, and λ is a positive constant.

260 Let e be the error of the chopper filter current, and if we set $n = 1$, then the

261 expression of the surface has the form:

$$262 \quad S(I_{fb}) = I_{fb_{ref}} - I_{fb} \quad (11)$$

263 The control law can be defined as:

$$264 \quad V_{fb} = V_{fb}^{eq} + V_{fb}^{fuzzy} \quad (12)$$

265 Where the filter voltage V_{fb} is chosen as the control vector which consists of two

266 control terms. The first term (V_{fb}^{eq}), called equivalent control, maintains the state of the

267 system on the surface, and the second term (V_{fb}^{fuzzy}) is the fuzzy controller which forces

268 the trajectory of the system to converge on the surface. The latter has the advantage of

269 eliminating the chattering effect.

270 By taking the derivative of the surface and substituting the expressions of the

271 filter current derivative and V_{fb} , gives:

$$272 \quad \dot{S}(I_{fb}) = \dot{I}_{fb_{ref}} - \frac{1}{L_{fb}} [(V_{fb}^{eq} + V_{fb}^{fuzzy}) - R_{fb}I_{fb}] \quad (13)$$

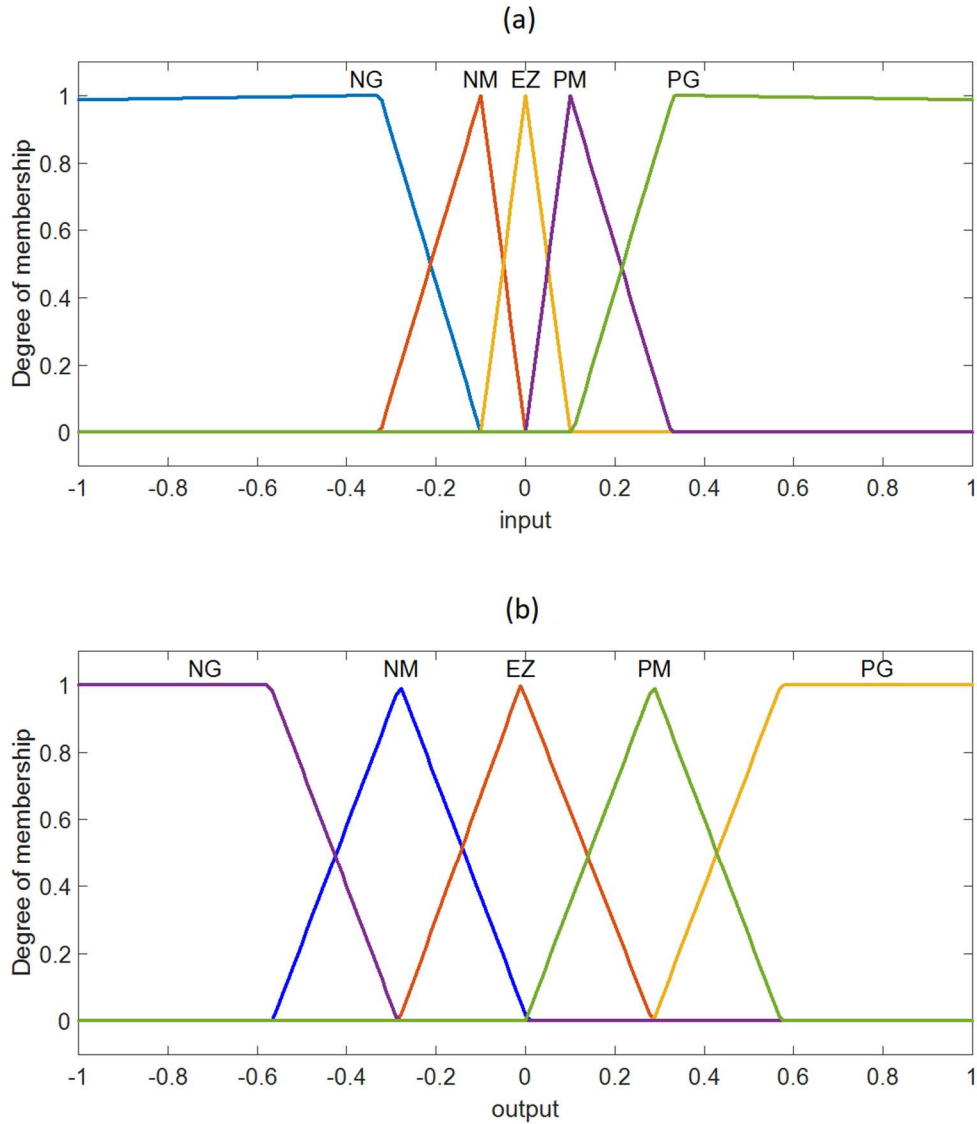
273 During the sliding mode, we have: $S(I_{fb}) = 0$, $\dot{S}(I_{fb}) = 0$, $V_{fb}^{fuzzy} = 0$. Hence,

274 the expression of the equivalent control is written:

$$275 \quad V_{fb}^{eq} = L_{fb}\dot{I}_{fb_{ref}} + R_{fb}I_{fb} \quad (14)$$

276 Substituting the expression of the equivalent control in Eq. (13), yields:

$$277 \quad \dot{S}(I_{fb}) = -\frac{1}{L_{fb}}V_{fb}^{fuzzy} \quad (15)$$



278

279 Figure 4. Membership functions: (a) input variables $S(I_{fb})$, (b) output variables V_{fb}^{fuzzy}

280

281 To ensure convergence towards the surface, the condition $S(I_{fb})\dot{S}(I_{fb}) \leq$

282 0 must hold. To achieve the convergence condition, the fuzzy sets, the membership

283 functions and the fuzzy rules are defined as follows:

284 For the input variable $S(I_{fb})$ and for the output variable V_{fb}^{fuzzy} , the fuzzy sets

285 are defined as: Positive Big (PB), Positive Medium (PM), Equal Zero (EZ), Negative

286 Medium (NM) and Negative Big (NB). The membership functions are depicted in Fig.4.
 287 Membership function aggregation and Mamdani’s fuzzy inference are based on the max
 288 operator. The center of gravity defuzzification method is used for the control output.
 289 Thus the rules which ensure the convergence condition are presented in Table 1.

290

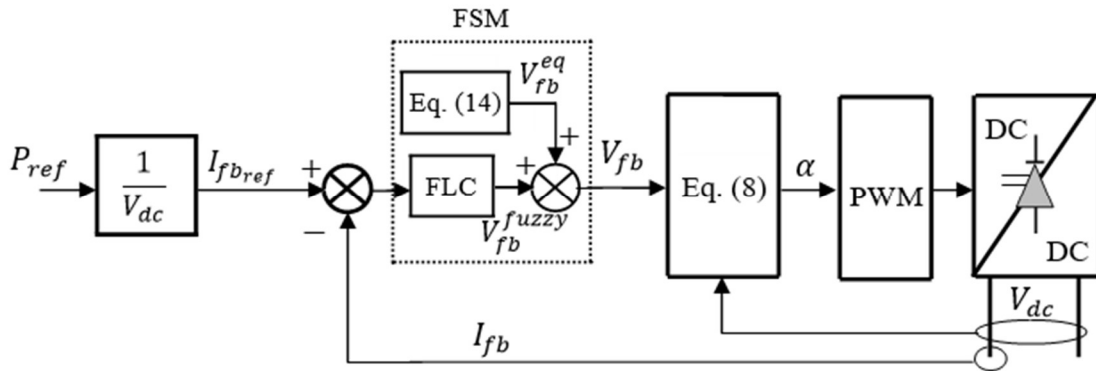
Table 1 Fuzzy rules

Rules	1	2	3	4	5
If $S(I_{fb})$ is	NB	NM	EZ	PM	PG
V_{fb}^{fuzzy} must be	NB	NM	EZ	PM	PG
Therefore according to the Eq. (15) $\dot{S}(I_{fb})$ is	PB	PM	EZ	NM	NG
Convergence condition	assured	assured	assured	assured	assured

291

292

Figure 5 shows the block diagram of the buck converter control with FSM.



293

294

Figure 5. FSM control scheme of the buck converter

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6. Simulation Results

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To validate the converter configuration and its control scheme proposed, two

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different case scenarios of real time simulations are carried out under RT-LAB. The first

300

scenario is aimed to assess the performance of the proposed PV system regardless of

301 the power imposed by the grid manager. In the second scenario, the inertial frequency
 302 response of the proposed PV system is tested under load variations and partial shading.

303

304 6.1 Performance assessment of the PV system and control scheme

305

306

Table 2 PV system parameter values

Parameter	Value	Unit
P_{pv}	3	kW
N_s	360	
N_p	2	
I_{sc}	7	A
I_{sat}	4.3e-7	A
V_{oc}	230.4	V
V_t	0.0257	V
K	1.38e-23	J/°K
e	1.602e-19	C
n	1.5	
R_s	1e-03	Ω
R_p	1e-03	Ω
R	0.25	Ω
R_{bf}	0.1	Ω
R_f	1	Ω
L_{bf}	402e-6	H
L_f	2.5e-3	H
C	12e-4	F
C_{dc}	3.6	F
ω_s		rad/sec
θ_s		rad
t_s	0.1	sec
ξ	0.707	
ω_0	27	rad/sec

307

308 In the first study, a 3 kW PVG connected to an infinite bus electrical grid is

309 considered. The parameters of the PVG are listed in Table 2. To assess the robustness of

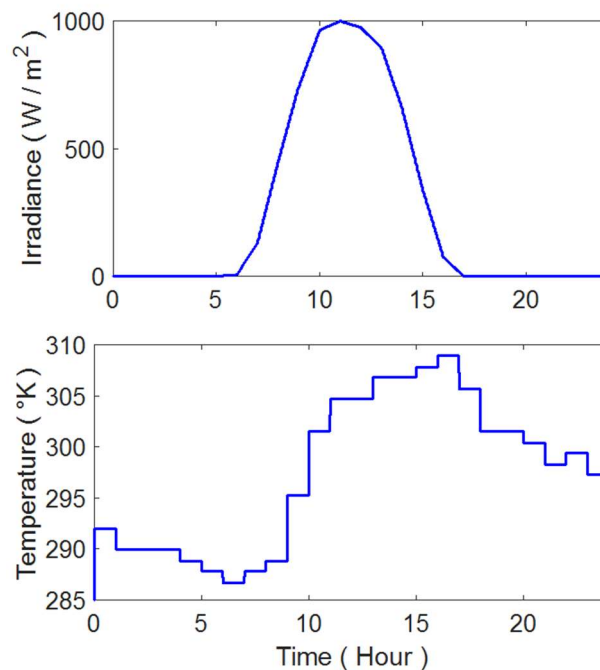
310 the control, the resistance R_{bf} was increased by 50%, and the inductance L_f was
 311 decreased by 50%.

312 The reference voltage of the DC bus is fixed at 180 V and the capacitors are
 313 initially charged at 180 V. The reference reactive power Q_{ref} is fixed at 0 VAR.

314 The first simulation test consists in imposing several reference power modes
 315 according to the grid code, then compare the results of the PI control to those of FSM
 316 control. The reference power modes are given as follows:

- 317 - Delta mode (Fig.7. Left panel): in this mode a reserve of 20% has been imposed
 318 ($P_{ref} = 0.8 P_{max}$).
- 319 - Balance mode (Fig.7. Right panel): in this mode, $P_{ref} = 1 \text{ KW}$ has been imposed.

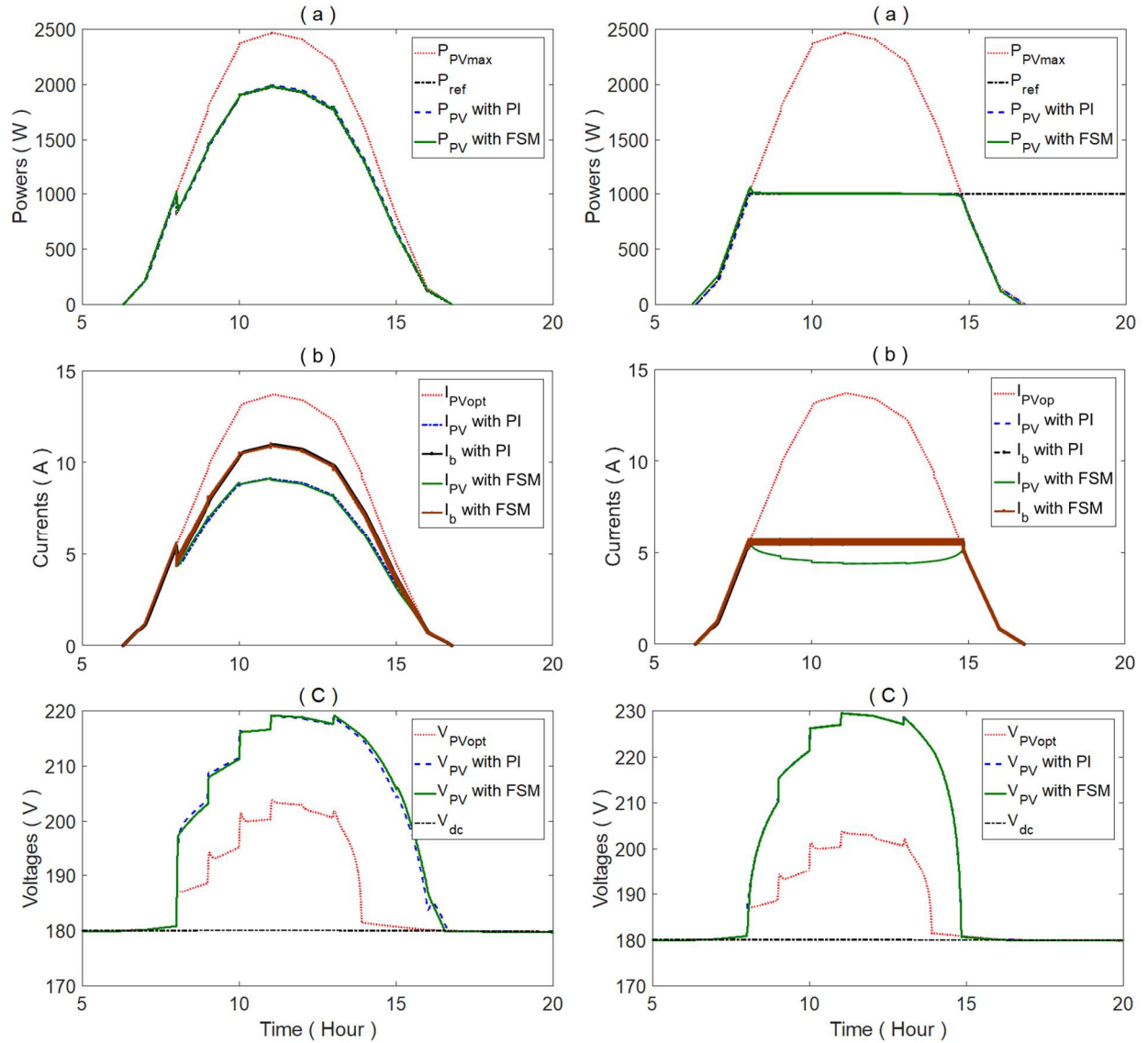
320 The system operation is simulated over a period of 24 hours, with varying
 321 temperature, irradiation (Fig.6).



322

323

Figure 6. Solar irradiance and temperature profiles used in the model



324
 325 Figure 7. Left panel: Responses in delta mode. Right panel: Responses in balance mode:
 326 (a) powers, (b) currents, (c) voltage
 327

328 From the results shown in Fig.7, the following conclusion can be drawn:

329 - A similarity between the responses obtained with the PI control and the FSM
 330 control.

331 - The power produced by the PVG perfectly follows its reference with a very small
 332 steady-state error, irrespective of PV of which mode is being used (see Fig 7.a).

- 333 - The responses of the currents are similar to those of the powers (see Fig 7.a and
334 Fig 7.b).
- 335 - The buck converter current (I_b) is always greater than the PV current (I_{pv}), and is
336 always lower than the optimal PV current ($I_{pv_{opt}}$), see Fig 7.b. It confirms that
337 the system is operating in the right side of the PVG characteristic.
- 338 - The voltage V_{dc} follows its reference and the voltage V_{pv} is variable due to the
339 extraction of the desired power. In addition, the V_{pv} voltage is always higher than
340 the voltages V_{dc} and $V_{pv_{opt}}$ (see Fig 7.c). Thus it also confirms that our system is
341 operating in the right side of the PVG characteristic.

342 Thus, according to these results, it can be noted that despite the variation of the
343 irradiation and temperature and regardless of the control mode, the proposed control
344 system is operating in the right side of the PVG characteristic. It is also able to adapt
345 quickly to the changing operating conditions and provides accurate regulation of the
346 voltages and currents to deliver the desired power required by the grid.

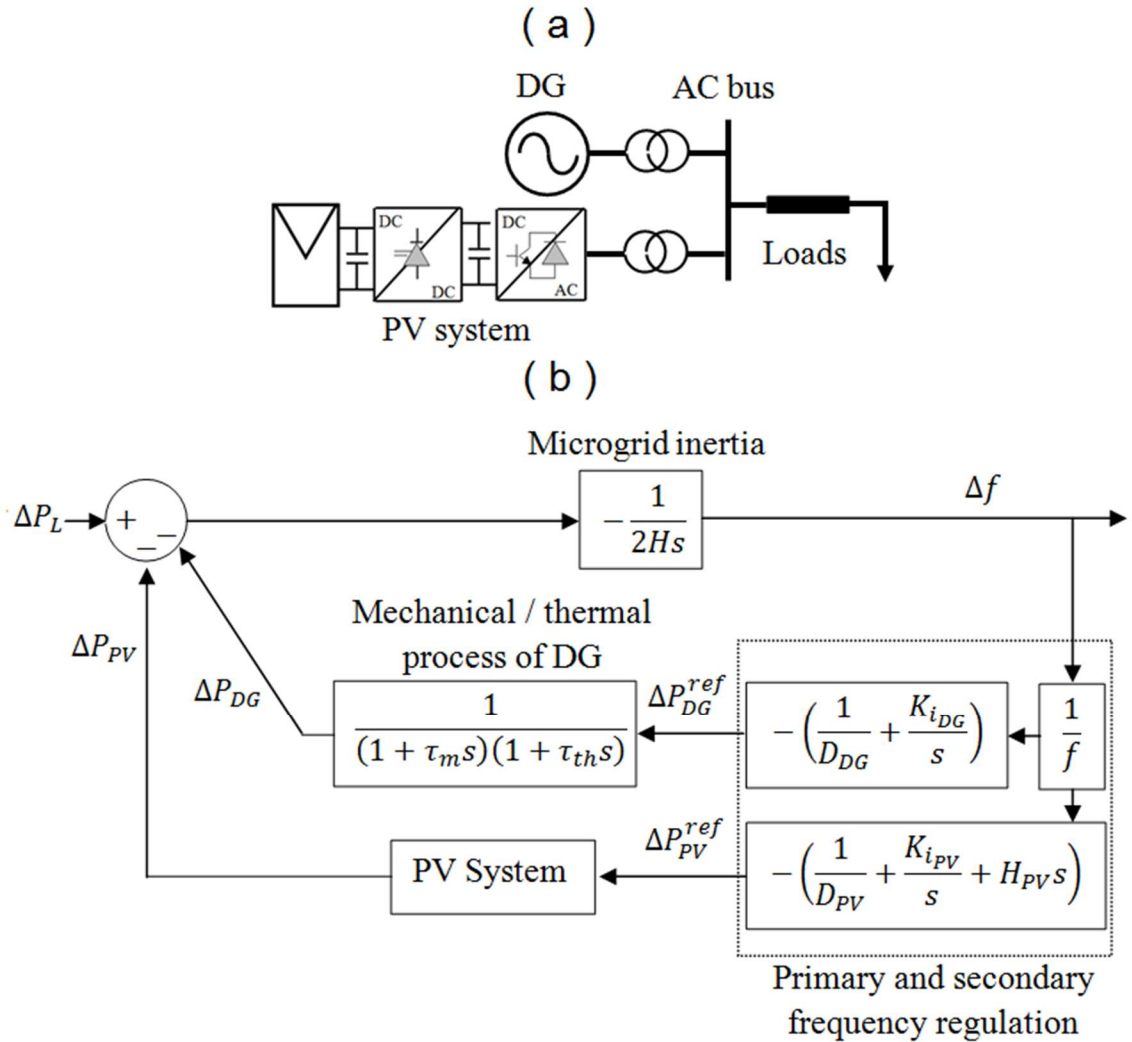
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348 **6.2. Performance assessment for grid frequency regulation**

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350 In the second study, a low inertia microgrid with high penetration of PV sources
351 is considered. As shown in Fig.8, the microgrid consists mainly of a DG diesel generator
352 (a voltage source which imposes the amplitude and frequency of the microgrid voltage),
353 the proposed PV system (a current source which imposes a power for a given voltage)
354 and variable loads (P_L). The mathematical model of the system considered is shown in

355 Fig.8.b. All the parameter values of the model are expressed in p.u. The parameters of
 356 the microgrid are listed in Table 3.
 357



358

359 Figure 8. Low inertia microgrid: (a) configuration (b) transfer function bloc

360

diagram

361

Table 3 Microgrid parameter values.

Parameter	Value	Unit
v_g	380	V
f	50	Hz
H	0.1	sec

H_{PV}	10	sec
$D_{PV} = D_{DG}$	5	%
$K_{i_{PV}} = K_{i_{DG}}$	20	sec ⁻¹
τ_m	0.1	sec
τ_{th}	0.2	sec

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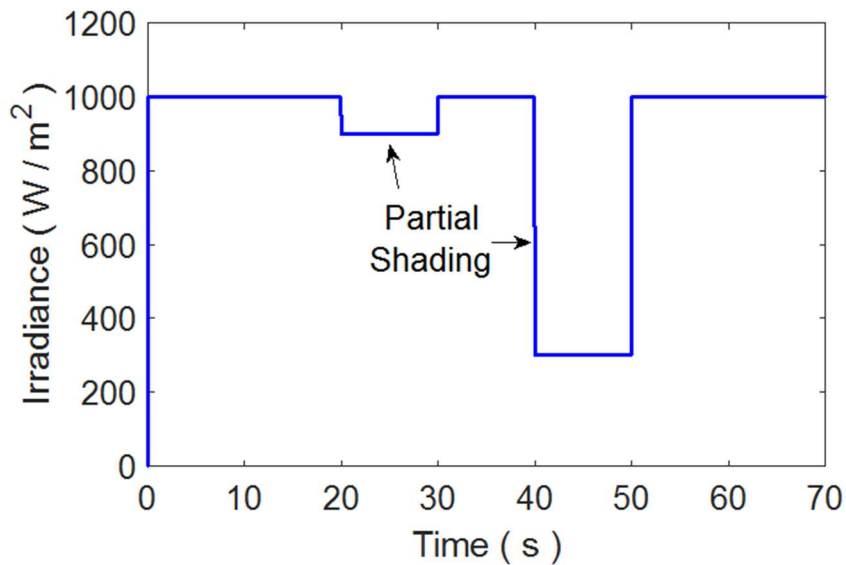
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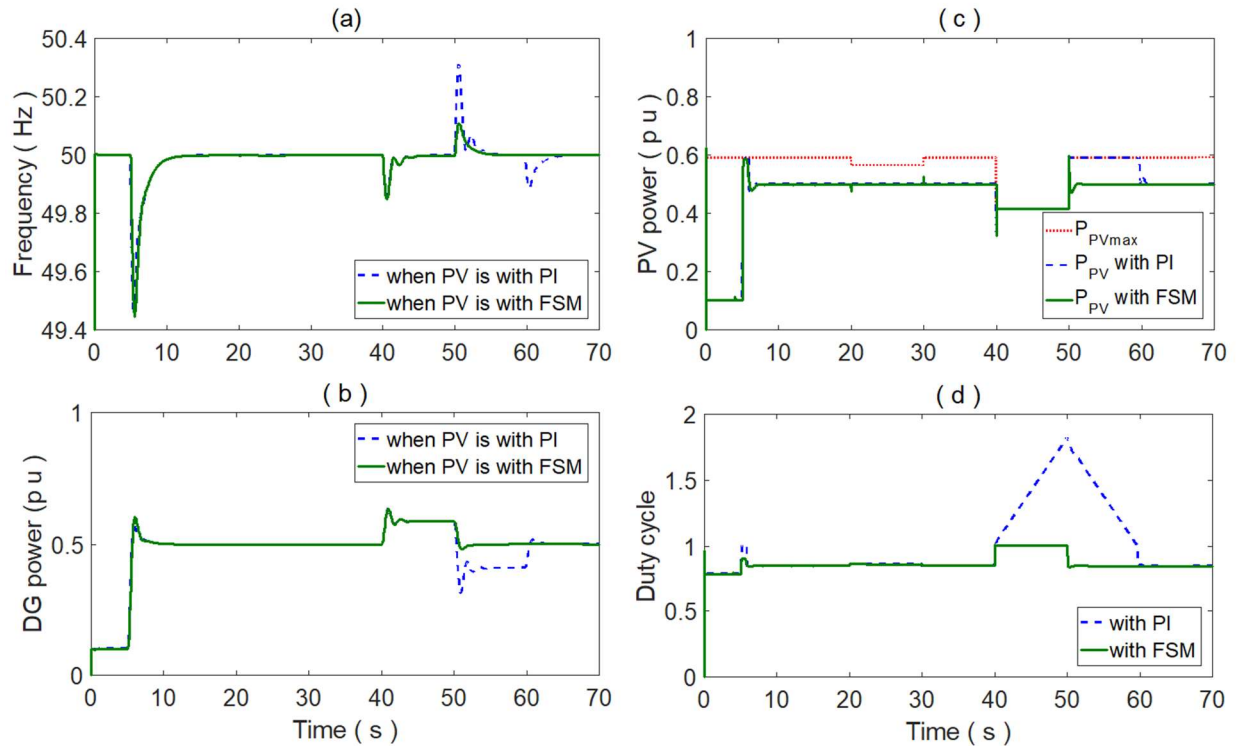
The aim of the study is then to involve the PV system in the grid frequency regulation by delivering the required real power according to the frequency measured locally. So the PV system must be able to add synthetic inertia (H_{PV}), to perform droop control (D_{PV}) and also to participate in secondary regulation which requires the addition of an integral gain ($K_{i_{PV}}$). All these parameters will be adjusted one after the other to limit the frequency deviation to 0.4 Hz when a load of 0.8 p.u. is used. Then imposing two different depth of a partial shading that will affect half of the PVG (Fig.9).



370

371

Figure 9. Partial shading profiles



372

373

Figure 10. Responses of (a) frequency, (b) DG power, (c) PV power, (d) duty cycle

374

during load variation and partial shading

375

From the results obtained in Fig.10, it can be concluded:

376

- When a load of 0.8 p.u. is applied at $t = 5$ s, the responses obtained with the PI

377

control and the FSM control are similar.

378

- When a partial shading with a slight depth is applied at $t = [20, 30]$ s (the

379

available PV power is greater than the requested power), there is a no change in

380

the responses.

381

- When a partial shading with a deep depth is applied at $t = [40, 50]$ s (the available

382

power is lower compared to the requested power), the FSM control remains

383

stable (the value of the duty cycle is kept at 1) and the PI controller becomes

384

unstable (increase in the value of the duty cycle), in contrast, the response of PV

385 power is maintained stable, due to the buck converter action which naturally
386 limited the V_{pv} decrease.

387 - At $t = [50, 60 \text{ s}]$ (no partial shading), the system quickly returns to normal
388 operation with FSM control, as compared to PI control which is much slower (the
389 delay can last longer if the time of the partial shading is large).

390 From these results, it can be noted that during the partial shading, the buck
391 converter was able to keep the system responses stable with the PI control. The FSM, on
392 the other hand, was able to make the control stable, robust and fast.

393
394 **7. Conclusion**
395

396 This article proposes a new two-stage power converter topology for a
397 Photovoltaic (PV) system without energy storage device and its control scheme based
398 on a hybrid robust fuzzy sliding mode control (FSM). The aim is to enable the PV power
399 system to support the grid inertia and contribute to frequency regulation.

400 Unlike other research, where the dc-dc boost converter has been widely used in
401 PV systems, our work has proposed to use the dc-dc buck converter instead. The
402 advantage of using the buck converter is the simplified design of the control law, which
403 allows the PV power system to operate in the right side of their characteristic and hence
404 will have the capacity to deliver power from almost zero to the maximum power.

405 Furthermore, the use of the FSM control provides additional robustness to the
406 control scheme under variable weather conditions such as temperature, solar
407 irradiance, and partial shading.

408 The proposed strategy has been validated via a series of real time simulations
409 using OPAL-RT system under different power constraints and different grid frequency
410 regulation schemes. The results obtained confirm the effectiveness, speed of response
411 and accuracy of the proposed solution.

412

413 **Appendix 1: Grid Code Technical Issues on Real Power Management**

414

415 Different grid codes to connect PV plants to the grid and provide for real power
416 controls are shown in Fig. 11.

417

418 **A. Absolute power constraint**

419

420 In using such control mode, the real power produced must not exceed a
421 maximum level predefined by the grid operator, even in the case of power excess.
422 Below the maximum level, the PV system is controlled to provide its available power.
423 The main reason for using this control mode is to avoid generating more power than
424 what is required by the load. Otherwise, the excess of power will have to be exported to
425 neighboring power grids at no charge. This is illustrated in Fig.11.a.

426

427 **B. Delta production constraint**

428

429 The Delta mode of control is used to limit the generation below the available
430 power with a fixed power reserve (ΔP) as depicted in Fig. 11.b. This control allows PV
431 energy to participate in the primary frequency regulation. If the latter decreases, the
432 source will be able to increase its output while keeping the frequency within acceptable
433 range. In addition, this control can help reduce fluctuations in real power.

434

435 **C. Power gradient constraint**

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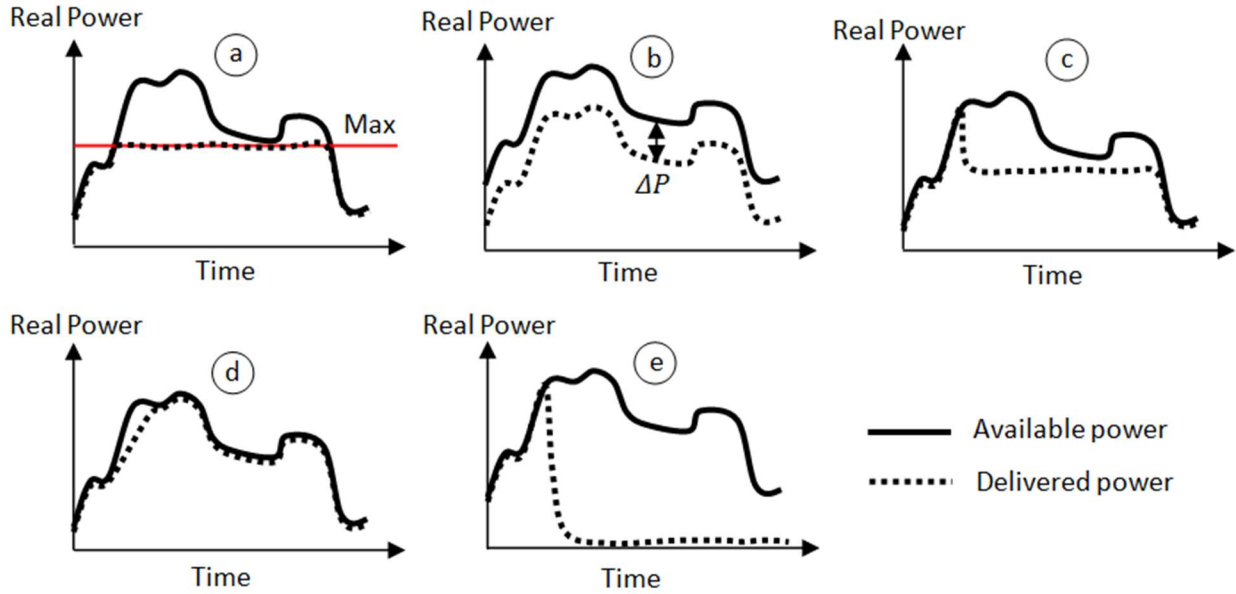
Here the power variation is limited to a certain speed kW/s. This constraint is used to prevent rapid changes in real power from affecting grid stability. To achieve its functionality, the PV farm should be equipped with an accurate tool for predicting very short-term solar irradiation and temperature in the order of 10 sec. This control mode is illustrated in Fig.11.c.

D. Balance control

During the balance control mode, the PV power plant must be able to increase or decrease its delivered power very quickly to support the electrical grid in balancing supply and demand of real power as shown in Fig. 11.d. The PV power plant then participates in the secondary frequency regulation and must be interfaced with the grid operator's dispatching station.

E. System protection

The system protection control type is used to protect the electricity grid when there is an excess of generation. In such a type of control, the PV power plant must quickly reduce its power output. The value of the PV power is predefined and updated by the grid operator. The reduction will be maintained until the issued command signal to trigger the protection is disabled. The approach is illustrated in Fig.11.e.



458

459

Figure 11. Constraints on real power management capabilities: (a) absolute

460

power constraint, (b) delta power constraint, (c) power gradient constraint, (d) balance

461

control, (e) system protection [26]

462

463

Appendix 2 Control of the dc/ac converter

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465

The objective of the dc/ac converter is to: (i) keep the DC bus voltage constant

466

regardless of the PVG power magnitude (optimal transfer of power to the electrical grid)

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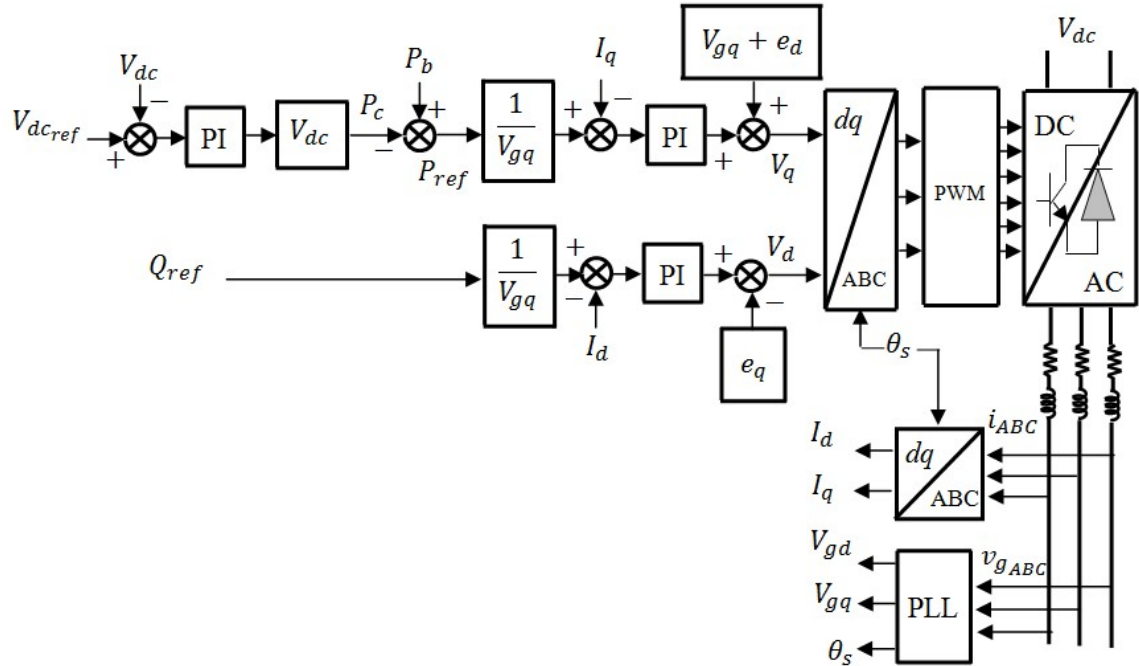
and (ii) ensure independent control of real and reactive powers between the DC bus and

468

the grid with a unity power factor. Fig.12 shows the block diagram of the DC-AC

469

converter control.



470

471

Figure 12. DC-AC converter control

472

473

A. Independent control of real and reactive power

474

475

The converter is connected to the grid through a three-phase filter. Thus, the

476

voltages of phase A, B and C at the terminals of the filter are written:

477

$$\begin{cases} v_A = -R_f i_A - L_f \frac{di_A}{dt} + v_{gA} \\ v_B = -R_f i_B - L_f \frac{di_B}{dt} + v_{gB} \\ v_C = -R_f i_C - L_f \frac{di_C}{dt} + v_{gC} \end{cases} \quad (16)$$

478

Applying Park's transformation to this three-phase voltage, gives:

479

$$\begin{cases} V_d = -R_f I_d - L_f \frac{dI_d}{dt} + \omega_s L_f I_q + V_{gd} \\ V_q = -R_f I_q - L_f \frac{dI_q}{dt} + \omega_s L_f I_d + V_{gq} \end{cases} \quad (17)$$

480

The real and reactive powers generated by the converter are defined by:

$$\begin{cases} P = V_{gd} \cdot I_d + V_{gq} \cdot I_q \\ Q = V_{gq} \cdot I_d - V_{gd} \cdot I_q \end{cases} \quad (18)$$

482 By choosing the rotating field as reference frame $d - q$ reference frame ($V_{gd} =$
483 0), Eq. (17) becomes:

$$\begin{cases} V_d = -R_f I_d - L_f \frac{dI_d}{dt} + e_d \\ V_q = -R_f I_q - L_f \frac{dI_q}{dt} + e_q + V_{gq} \end{cases} \quad (19)$$

485 Where the coupling terms between the two axes d and q are written as:

$$\begin{cases} e_d = \omega_s L_f I_q \\ e_q = \omega_s L_f I_d \end{cases} \quad (20)$$

487 Taking into account the orientation of the reference $d - q$ related to the
488 rotating field, Eq. (18) becomes:

$$\begin{cases} P = V_{gq} I_q \\ Q = V_{gq} I_d \end{cases} \quad (21)$$

490 Thus, the real and reactive powers are controlled by I_q and I_d respectively.

491 The model of the converter connected to the grid expressed in the rotating field
492 reference frame given by Eq. (19) shows that we can set up a controller for each current
493 component flowing in the filter. The reference quantities for these controllers will be
494 the filter currents in the $d - q$ axes.

495

496 **B. DC bus voltage control**

497

498 By neglecting the harmonics due to switching and all losses in the converter and
499 filter, the powers of the DC bus are:

$$\begin{cases} P = V_{dc}I \\ P_b = V_{dc}I_b \\ P_c = V_{dc}I_c \end{cases} \quad (22)$$

501 Where P is the real power flowing through the dc-ac converter, P_b is the output
 502 power of the buck converter, and P_c is the power required to charge the capacitor C_{dc} .

503 These powers are related by the following equation:

$$P = P_b - P_c \quad (23)$$

505 By controlling the power P , the power P_c in the capacitor can then be controlled
 506 and subsequently the voltage of the DC bus can be controlled. Therefore, the powers P_b
 507 and P_c must be known to determine P_{ref} .

508 The reference power for the capacitor is related to the reference current flowing
 509 in the capacitor as follows:

$$P_{c_{ref}} = V_{dc}I_{c_{ref}} \quad (24)$$

511 The DC bus voltage is then controlled by an external loop (with respect to the
 512 internal current regulation loop), with a controller generating the reference current $I_{c_{ref}}$
 513 in the capacitor.

514 515 **C. Determination of the PI controller parameters** 516

517 The control of the converter involves three controllers, two for controlling the
 518 filter currents and one controller for the DC bus voltage. To design the control loops, it is
 519 assumed that the converter is ideal, and the coupling terms are neglected. It can be
 520 noticed that the transfer functions of the two currents are identical therefore the two
 521 controllers will have the same parameters.

522 The block diagram of the closed-loop current control I_q is shown in Fig.13.a.

523 The PI controller parameters are calculated so that the closed-loop transfer

524 function is first order.

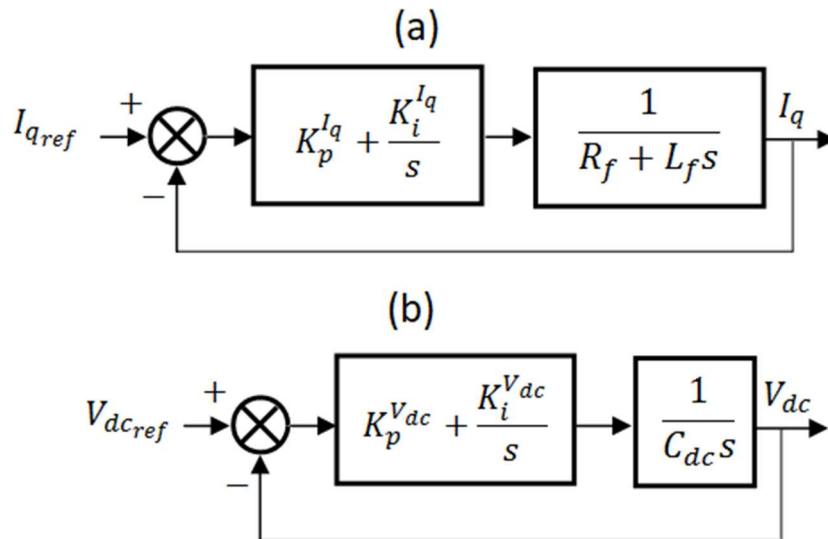
$$525 \quad \begin{cases} K_p^{I_q} = \frac{3L_f}{t_s^{I_q}} \\ K_i^{I_q} = \frac{3R_f}{t_s^{I_q}} \end{cases} \quad (25)$$

526 The block diagram of the closed-loop DC bus voltage control is shown in Fig.13.b.

527 The PI controller parameters are calculated so that the closed-loop transfer

528 function is second order with damping ξ and natural frequency ω_0 .

$$529 \quad \begin{cases} K_p^{V_{dc}} = 2\xi C_{dc}\omega_0 \\ K_i^{V_{dc}} = C_{dc}\omega_0^2 \end{cases} \quad (26)$$



530

531

Figure 13. Control loop: (a) current component I_q , (b) DC bus voltage

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537

538 **NOMENCLATURE**

C	Capacitance, F
C_{dc}	DC bus capacitance, F
e	Elementary charge on an electron, C
f	Grid frequency, Hz
H	Grid inertia constant, sec
I_{sat}	Diode saturation current, A
K	Boltzman constant, J/°K
L_{bf}	Buck filter inductance, H
L_f	Grid filter inductance, H
N_s	PV array series number
N_p	PV array parallel number
n	Quality factor
P_{pv}	PV system capacity, W
R_s	Series resistance, Ω
R_p	Parallel resistance, Ω
R	Resistance, Ω
R_{bf}	Buck filter resistance, Ω
R_f	Grid filter resistance, Ω
t_s	Settling time, sec

v_g Grid voltage, V

V_{oc} Open circuit voltage in standard condition, V

V_t Thermal voltage, V

539 **Greek Letters**

θ_s Position angle, rad

ω_s Synchronous speed, rad/sec

ω_0 Cut-off frequency, rad/sec

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657
658**Figure Captions List**

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