

# A Wideband 1.5-bit unit cell for Reconfigurable Reflectarray design

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**Abstract**—This paper presents a low-complexity, wideband unit cell design suitable for reconfigurable reflectarrays (RRAs). To overcome the challenges of high implementation complexity associated with high-bit quantization, and the performance degradation resulting from one-bit quantization, a 1.5-bit phase quantization approach is proposed. The developed unit cell can operate in multiple modes to realize reflection phase delay, resonance tuning, and true-time delay, controlled by two PIN diodes. By switching the states of these diodes, the unit cell provides four reflection states. Wideband operation is achieved by selecting appropriate states across the frequency range to ensure continuous phase coverage. To validate the proposed concept, a unit cell was designed and simulated. The simulation results demonstrate stable three-phase responses with low reflection amplitude variation under different incidence angles across the 13.4–19.2 GHz frequency band.

**Index Terms**—Reflectarray, metasurface, 1.5-bit, wideband, reconfigurable.

## I. INTRODUCTION

The application of reconfigurable reflectarray (RRA) in the next generation mobile communication [1], satellite communication [2], remote sensing [3], and other fields has attracted much attention. The RRA combines the advantages of traditional phased array and parabolic antenna, and has the characteristics of high gain, real-time beam agility and low complexity [4], [5], [6].

Tunable devices or materials such as p-i-n diodes, varactors, MEMS switches, and liquid crystals are used to achieve beam steering of the RRA. The varactor [7], [8] can achieve a continuous reflection phase range, but the insertion loss is large, the bias circuit is complex, and the voltage regulation accuracy is high. MEMS [9], [10] switches have advantages in low insertion loss, good electrostatic control, and monolithic integration. However, the high cost and low switching response of MEMS components affect their large-scale implementation. Liquid crystals [11], [12] have attracted interest because of their transparency and mechanical flexibility, but they have large insertion losses and delays.

p-i-n diodes are widely used in the design of RRA due to their low insertion loss, fast switching response and low price. Although p-i-n diodes have many attractive characteristics, they can only provide discrete phase states, resulting in some quantization loss. Yang et al. [13] analyzed the phase error caused by discrete phases. The phase quantization loss of 1-bit, 2-bit and 3-bit phase discretization is about 3 dB,

0.6 dB and 0.2 dB, respectively. 1-bit RRA has received extensive attention and research because it can significantly reduce the design complexity [14], [15], [16]. In [14], an ultra-broadband RRA is presented, employing both the mirror-symmetry principle and a sub-wavelength mechanism. The design achieves a 3 dB gain bandwidth of 44.4% and a maximum aperture efficiency of 20.9%. In comparison, RRAs with 2-bit phase quantization possess improved performance in terms of reducing SLL and increasing aperture efficiency. The implementation of 2-bit RRA is similar to that of 1-bit RRA, where a reflection phase difference of 90° can be obtained when p-i-n diodes are switched between the ON and OFF states [17], [18], [19], [20]. In [20], a polarization-insensitive 2-bit RRA is proposed by integrating two pairs of orthogonal p-i-n diodes on the top layer, and a square-ring slot and a vertical slot are used to suppress the cross-polarization level. It achieved an aperture efficiency of 31.2% with a 3 dB gain bandwidth of 8.6%.

Generally speaking, more switching states require more p-i-n diodes, which increases the complexity of the unit cell design and the overall controlling circuit. Moreover, the achievable bandwidth of the phase delay of each phase state would also have an impact on the overall bandwidth of the RRA. To address these challenges, this paper presents a low-complexity RA unit cell design based on 1.5-bit phase quantization. The paper is organized as follows. Section II introduces the structure of the proposed 1.5-bit RRA unit cell. Section III presents the analysis of the unit cell performance under both vertical and oblique incidence. Section IV analytically calculates the radiation pattern of a 12×12 reflectarray. Section V concludes this paper.

## II. UNIT CELL DESIGN

### A. RRA Unit Cell Design

The configuration of the proposed 1.5-bit RRA unit cell is shown in Fig. 1. It is composed of four layers and three dielectric substrates. The aperture size is  $0.44\lambda_0 \times 0.44\lambda_0$  at the center frequency, 16.5 GHz. The top layer is a rectangular patch with a U-shaped slot and a parasitic patch. The second layer is a rectangular patch and a square ring. A metalized via hole is used to connect the top and second layers. The use of U-slot loading enhances the design flexibility, and the double-layer metal patch forms a double resonance structure, which increases the phase shift bandwidth. One p-i-n diode is placed

between the rectangular patch and the parasitic patch to change the phase shift of the unit cell. The third layer is the ground layer, which provides isolation between the DC bias lines and the receiving patch. The bottom layer includes a phase-shifting structure with one p-i-n diode and a fan-shaped open-end stub. Note that the two p-i-n diodes share a common ground, which reduces the complexity of the controlling circuit. The first and second dielectric substrates are FBM220 ( $\epsilon_r = 2.2$ ,  $\tan\delta = 0.01$ ,  $h_1 = 0.5$  mm,  $h_2 = 1$  mm). The third dielectric substrate is Rogers 4003 ( $\epsilon_r = 3.3$ ,  $\tan\delta = 0.027$ ,  $h_3 = 0.2$  mm). A bonding layer with a thickness of 0.2 mm is used between these substrates. The MACOM MA4AGP907 p-i-n diodes are chosen for this design. The equivalent circuit of the pin diode is shown in Fig. 2. The diodes exhibit an on-state resistance  $R_{on} = 4\Omega$  with series inductance  $L_{on} = 110pF$ , and off-state capacitance  $C_{off} = 30pF$  with series inductance  $L_{off} = 24fF$ .

### III. RECONFIGURABLE REFLECTION PHASE

The developed unit cell uses two different modes to realize reflection phase delay, resonance tuning and true-time delay. Unlike conventional 2-bit RRA unit cells where all four states operate at the same frequency band, the present design has two states for dedicated high- and low-frequency operation, respectively, while maintaining the other two wideband operation states that cover the full operational bandwidth. Low insertion loss and stable 3-phase switching in all sub-bands are achieved in the design. This method enables the switching of three distinct phase states over a wide frequency range, resulting in the realization of a broadband 1.5-bit RRA unit cell design.

The scattering parameters of the unit cell were simulated in the high-frequency structure simulator (HFSS). The simulation setup employed periodic boundary conditions and a Floquet Port excitation. The p-i-n diodes are modeled using RLC boundaries for both ON and OFF states. The four working states of the cell are referred to as “state 1,” “state 2,” “state 3,” and “state 4,” when the two p-i-n diodes are switched between “ON” and “OFF” states. As shown in Table I, the four operating states correspond to the following diode configurations: State 1: both diodes OFF; State 2: PIN1 ON and PIN2 OFF; State 3: PIN1 OFF and PIN2 ON; State 4: both diodes ON. As shown, States 1 and 2 provide broadband coverage (13.4-19.2 GHz), while States 3 and 4 are dedicated to the high (16.5-19.2 GHz) and low (13.4-16.5 GHz) frequency sub-bands, respectively. The simulated results, including reflection amplitude and phase under normal and oblique incidence, are shown in Fig. 3. Through switching the four states and each time selecting three corresponding phase states to cover the desired frequency band, the unit cell exhibits a reflection amplitude better than -2.4 dB and supports stable three-phase switching in the frequency range of 13.4-19.2 GHz, as demonstrated in Fig. 3(a). Fig. 3(b) shows the simulated results of the unit cell when the incident angle of the electromagnetic wave varies within  $30^\circ$ . It can be seen that the phase and

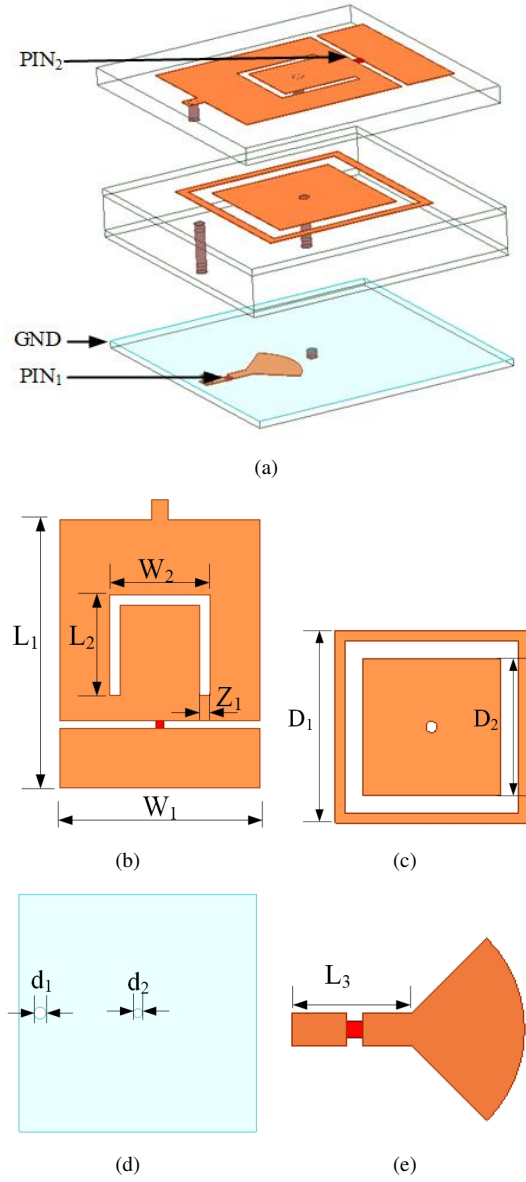


Fig. 1. The configuration of the proposed 3-phase cell: (a) exploded view, (b) first layer, (c) second layer, (d) third layer, and (e) fourth layer.

amplitude responses of the RRA unit cell are stable under different incident angles with a wide bandwidth.

TABLE I  
OPERATING STATES OF THE PROPOSED UNIT CELL

| State   | PIN 1 | PIN 2 | Operating Mode   | Frequency Range (GHz) |
|---------|-------|-------|------------------|-----------------------|
| State 1 | OFF   | OFF   | Original state   | 13.4–19.2             |
| State 2 | ON    | OFF   | Phase delay      | 13.4–19.2             |
| State 3 | OFF   | ON    | Resonance tuning | 16.5–19.2             |
| State 4 | ON    | ON    | Resonance tuning | 13.4–16.5             |

### IV. ANALYTICAL RESULTS

The response of a reflectarray consisting of  $12 \times 12$  unit cells is analytically calculated by extracting the incident field

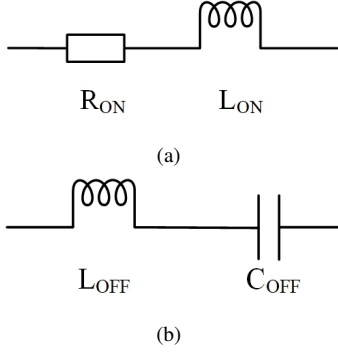


Fig. 2. Extracted equivalent circuit parameters of p-i-n diode at (a) ON state, (b) OFF state.

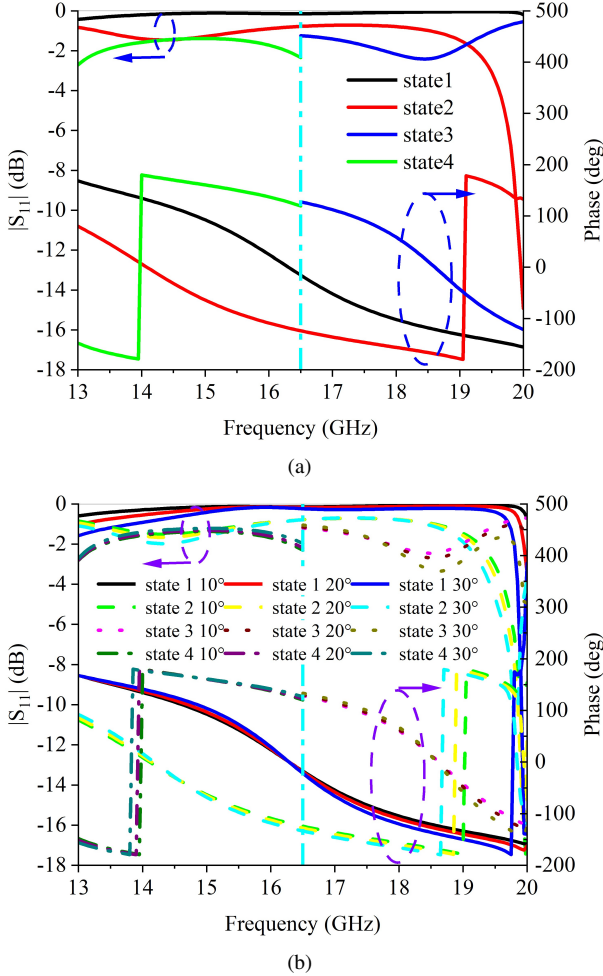


Fig. 3. Simulated reflection coefficients of the proposed RRA unit cell at (a) Vertical incidence, (b) Oblique incidence.

of the feed antenna and the radiation pattern of each unit cell. The calculated radiation pattern is presented in Fig. 4. The theoretical gains at 16 GHz and 17 GHz are 20.62 dBi and 20.68 dBi, respectively, with corresponding aperture efficiencies of 35% and 31.5%. The results demonstrate that the designed 1.5-bit reconfigurable unit cell increases the aperture efficiency of the array compared to conventional 1-bit

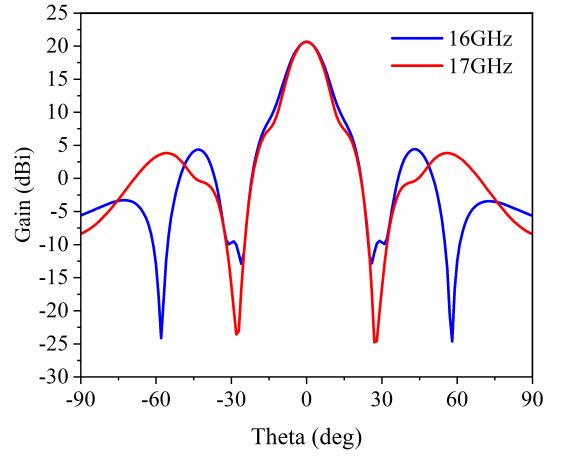


Fig. 4. Calculated radiation pattern of the reflectarray with  $12 \times 12$  unit cells at 16GHz and 17GHz

designs.

## V. CONCLUSION AND FUTURE WORK

In conclusion, this paper proposes a wideband unit cell for RRAs. This unit cell utilizes different operating modes to obtain phase delays in a wide frequency range, including two states for separate high- and low-frequency operation, while maintaining the other two states operating in the entire operational bandwidth. The simulation results show that the unit cell achieves a low reflection amplitude with stable phase switching over the frequency range of 13.4–19.2 GHz.

The future work of this study is to design and optimise the DC biasing line for the developed unit cell, and use full-wave EM simulations to validate the performance of the RRA before fabricating and measuring the prototype.

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